

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Theel

Serial No.: 10/724,984

Filed: November 30, 2003

For: Method For Optimizing MRAM Circuit
Performance

Art Unit: 2827

Examiner: Huan Hoang

Attorney Docket No.: 783403605018

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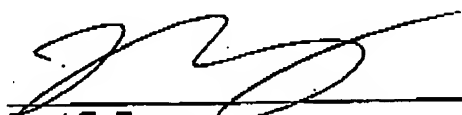
Sir:

This Statement is submitted in compliance with 37 C.F.R. § 1.56.

A list of patent(s) and/or publication(s) not previously submitted is set forth on the attached Form PTO-1449.

Any fees required for the proper filing of this Information Disclosure Statement should be withdrawn from Jones Day's Deposit Account No. 50-1432, account 783403605018.

Respectfully submitted,



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PTO/SB/03A (07-05)

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STATEMENT BY APPLICANT**

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Complete if Known

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| Application Number | 10/724,984 |
| Filing Date | November 30, 2003 |
| First Named Inventor | Wayne Theel |
| Art Unit | 2827 |
| Examiner Name | Huan Hoang |
| Attorney Docket Number | 783403-605018 |

Sheet 1 of 1

U. S. PATENT DOCUMENTS

| Examiner Initials* | Cite No. | Document Number | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
|-----------------------|-------------|--|--------------------------------|--|---|
| | | Number-Kind Code ² (if known) | | | |
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FOREIGN PATENT DOCUMENTS

| Examiner Initials* | Cite No. | Foreign Patent Document | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear | T ^o |
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